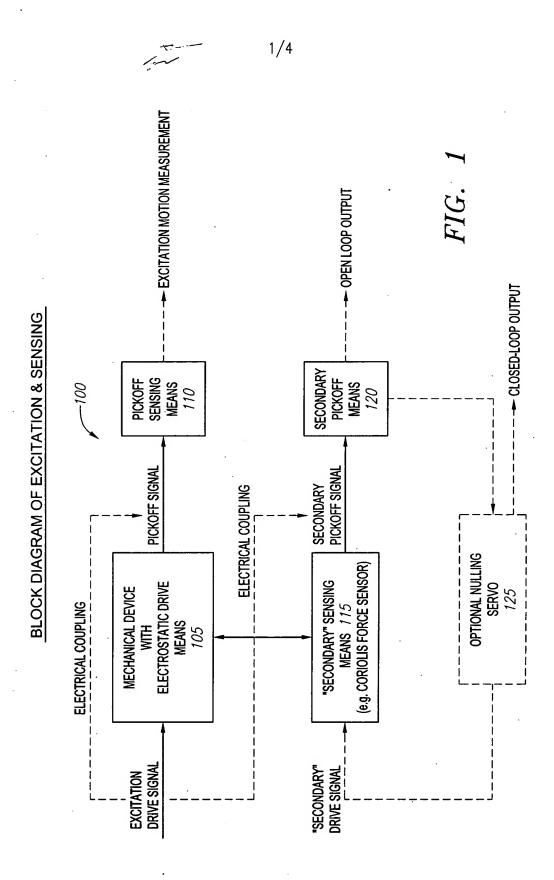
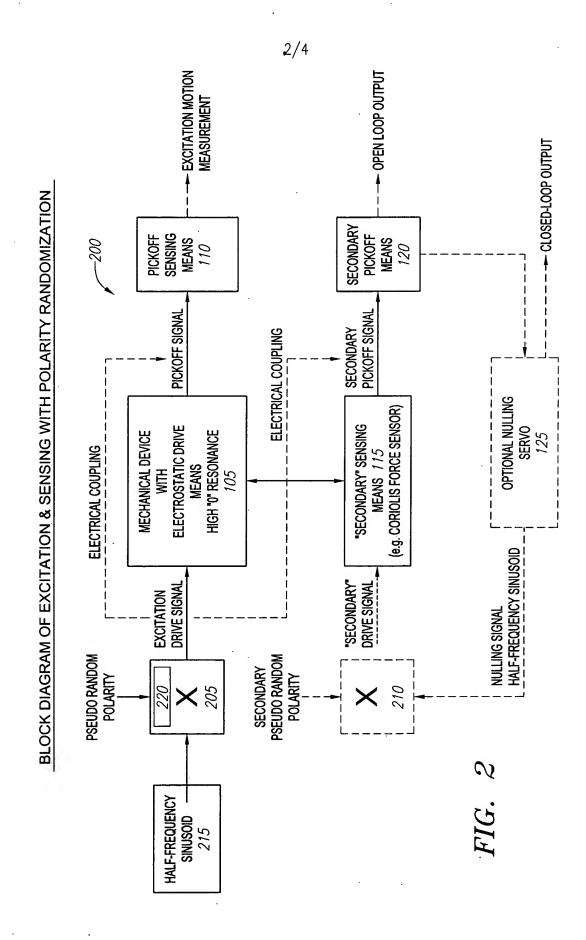
MODULATION METHOD FOR SIGNAL CROSSTALK MITIGATION IN ELECTROSTATICALLY DRIVEN DEVICES

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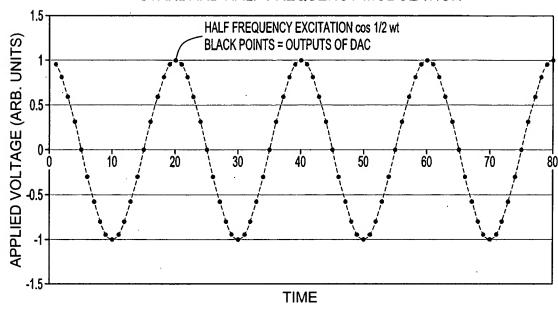


FIG. 3

HALF FREQUENCY MODULATION WITH POLARITY RANDOMIZATION

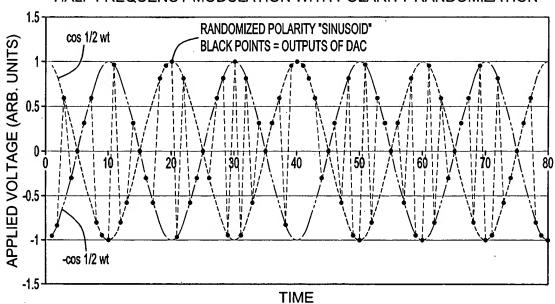


FIG. 4

MODULATION METHOD FOR SIGNAL CROSSTALK MITIGATION IN ELECTROSTATICALLY DRIVEN DEVICES

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HALF FREQUENCY MODULATION WITH POLARITY RANDOMIZATION AT ZERO CROSSINGS

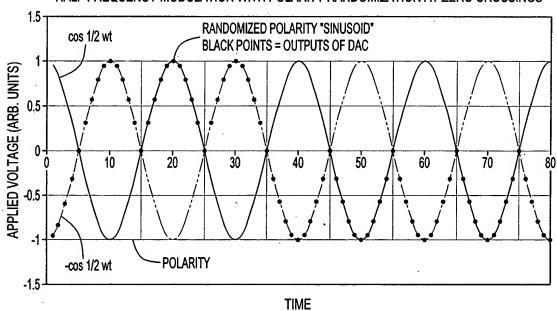


FIG. 5